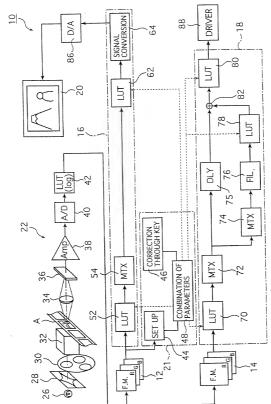
FIG.1



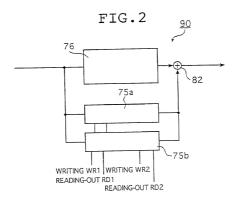
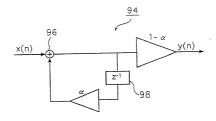


FIG.4



GATE INPUT A B GATE OUTPUT 2
PIXEL CLOCK 116b

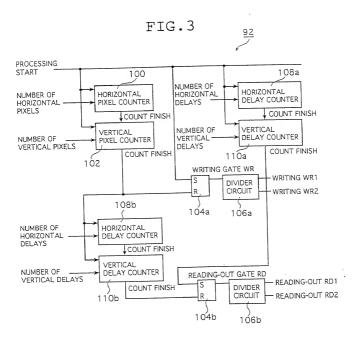


FIG.5

